

Implementing an Exception Mechanism in C

CS520

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```
int catchException(void);
```

```
void cancelCatchException(void);
```

```
void throwException(int);
```

So exception is just an int value

multiple calls to catchException can
be active

```
try {  
    ①  
}  
catch (e) {  
    ②  
}
```

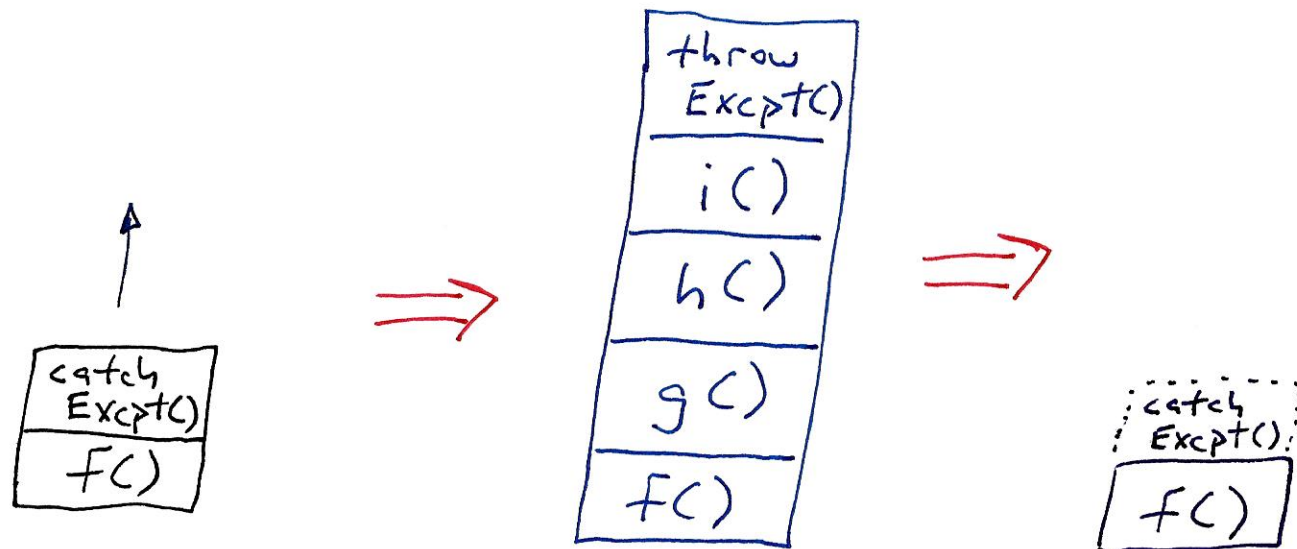
⇒

```
int e;  
if (e = catchException()) {  
    ②  
}  
else {  
    ①  
    cancelCatchException();  
}
```

note code block ② could re-throw the exception if it is one it cannot "handle."

Key to implementation

understand how Intel 64 stack frames work



so need to pare stack back

provide new return from catch Exception

catch Exception

take "snapshot" of stack

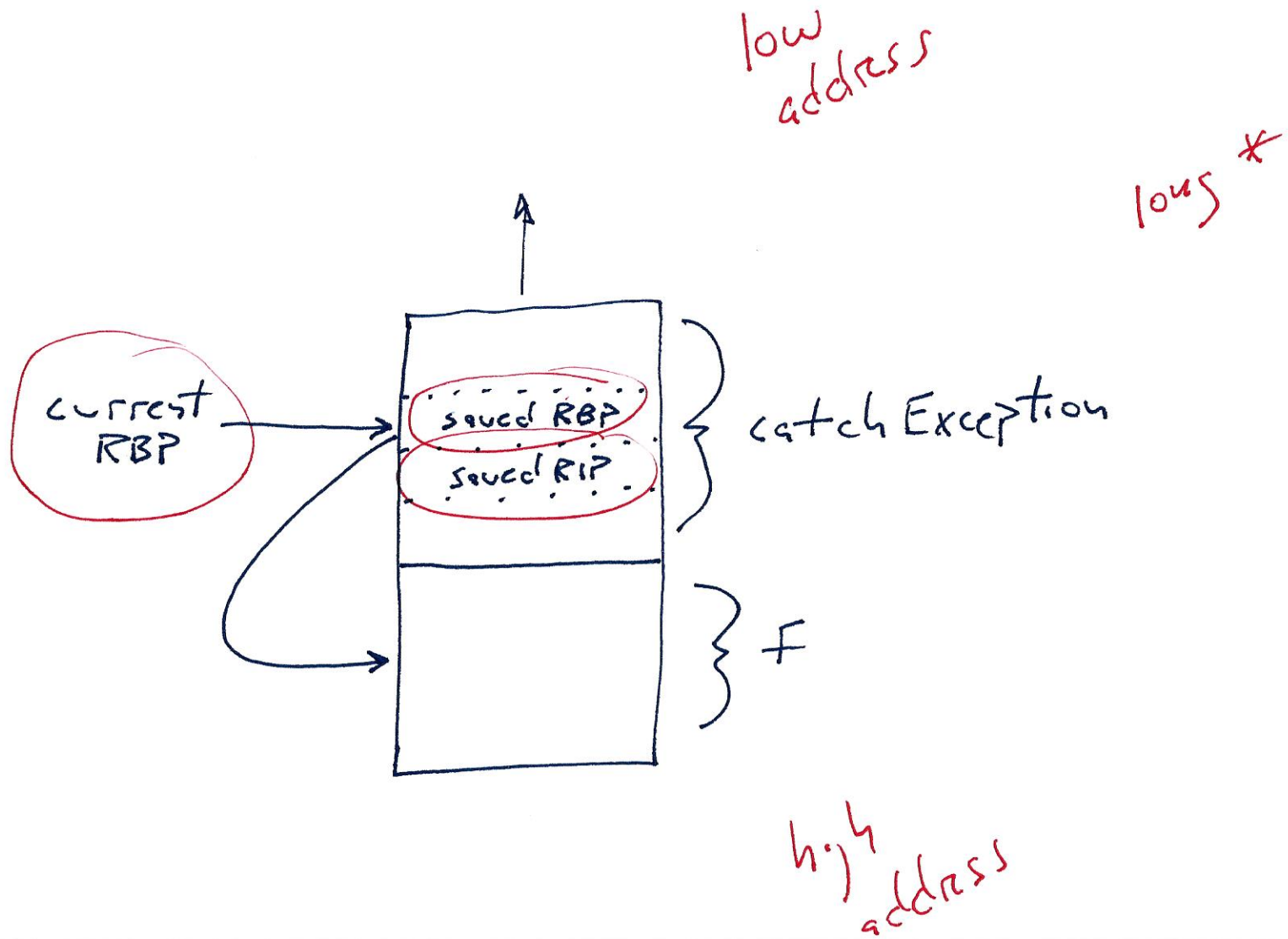
└─> current RBP & caller's RBP
└─> saved RIP

also save other callee-saved registers

└─> rbx & R12-R15

store the contents of these registers
into a struct

since there can be multiple catch Exceptions
alive at one time, need to maintain
a stack of these structs



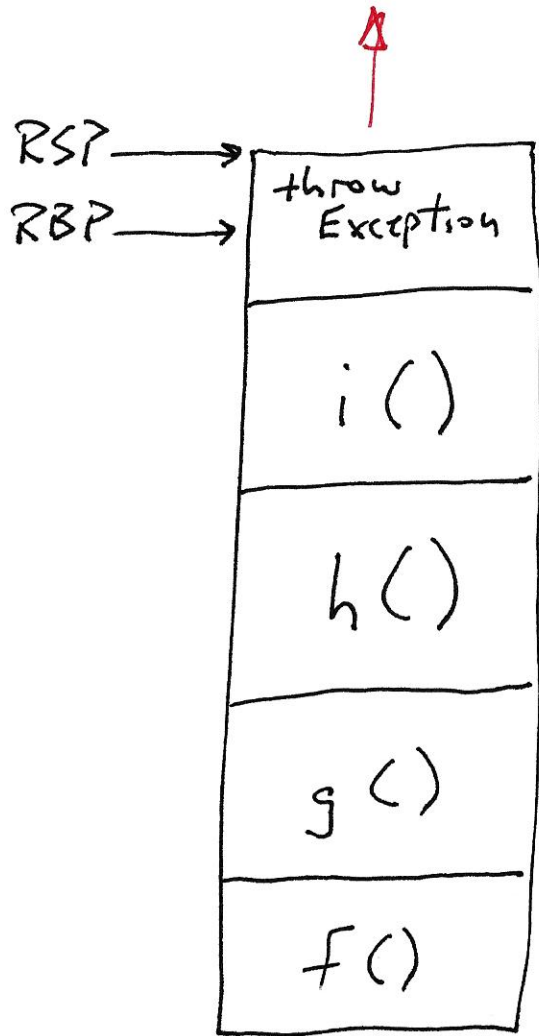
throw Exception

restore runtime stack using snapshot
in struct on top of stack of structs

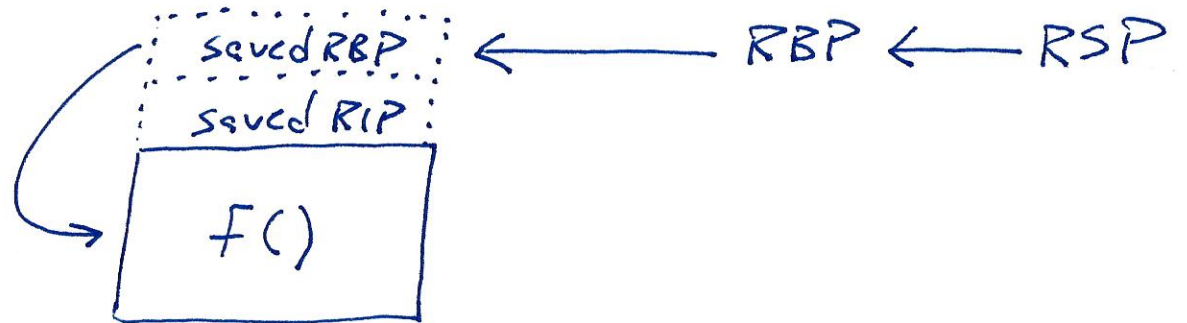
pop the stack of structs

restore registers

perform second return from catch Exception



set RAX 7
POP %rbp
ret



need assembly language routines

get RBP
get RBX
get R12
get R13
get R14
get R15

} get register contents

↳ maybe also routines to
set RBX, set R12, etc.?

plus routine to:

pare the stack

restore registers

setup and perform second return

from catch Exception

↳ put exception number into RAX

Cancel Catch Exception

pop the stack of structs