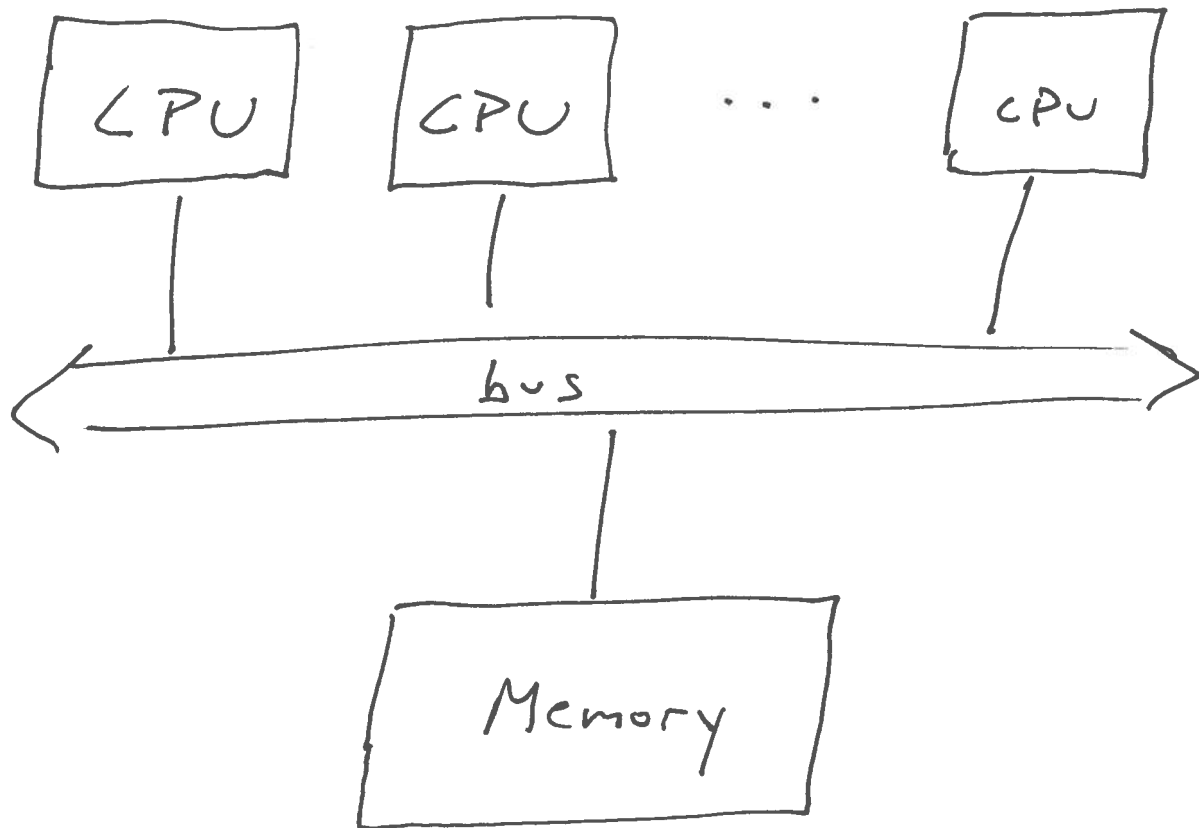


Threads

CS520

Dept. of Computer Science  
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thread: a virtual CPU

executes instructions

has registers

uses stack

useful for performance - exploit multiple CPUs

useful for design - e.g. assign thread to wait for GUI events while another thread does main computation

But

memory is shared

threads need to contend for it

threads can interfere with each other

Consider two threads incrementing the same variable.

<u>T<sub>0</sub></u>	$i = \phi$	<u>T<sub>1</sub></u>
$i++$		$i++$

$i = ?$

not atomic

First, what is  $i++$  actually?

load  $i$   
add 1  
store  $i$

Need to consider interleavings of the low-level instructions:

$$i = \phi$$

T<sub>0</sub>  
 $\phi \leftarrow \text{load } i$   
 $\text{add } 1$   
 $\text{store } i \rightarrow 1$

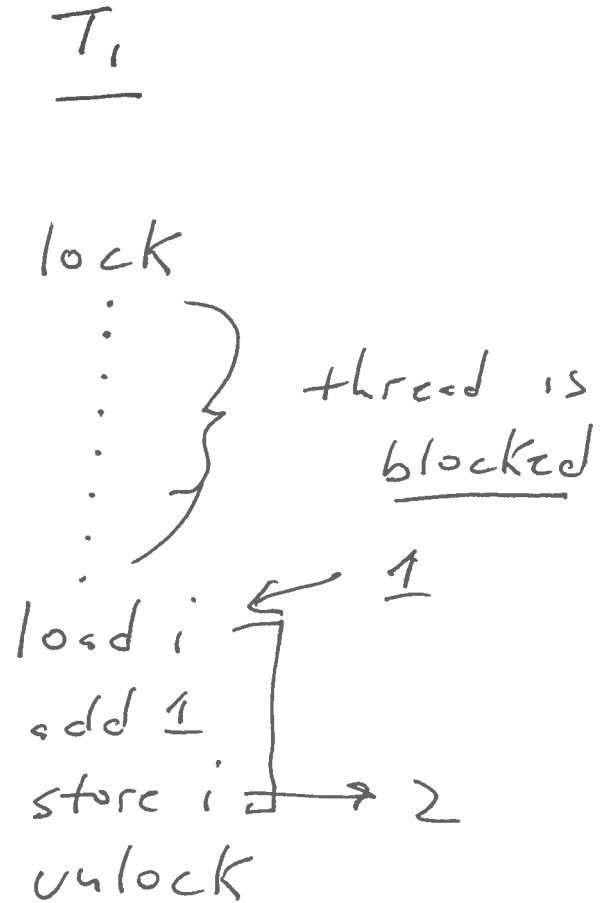
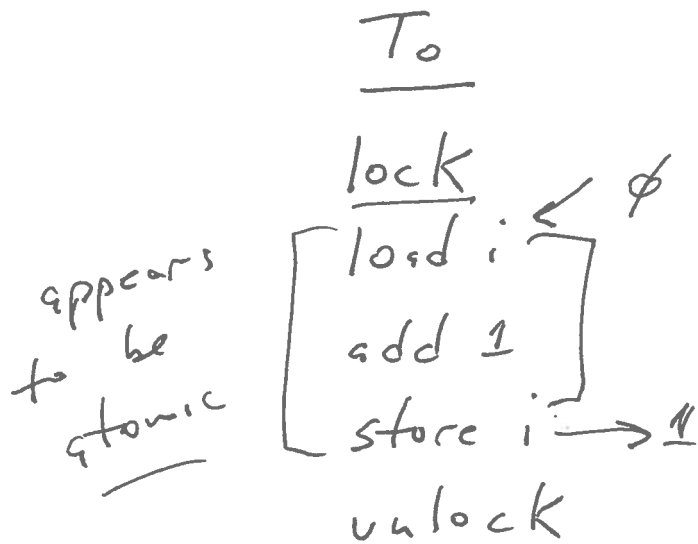
T<sub>1</sub>  
 $\text{load } i \rightarrow \phi$   
 $\text{add } 1$   
 $\text{store } i \rightarrow 1$

$$i = \cancel{1}$$

race condition: possibility of incorrect results due to timing

need mechanism to ensure only one thread at a time is in the critical section:

$$i = \phi$$



$$i = \cancel{1} 2$$

Also need mechanism to control interaction of threads.

For example, consider a thread producing a series of values to be consumed by another thread.

consumer thread can't consume until producer produces value

producer thread can't produce until last value consumed

assuming a buffer of length 1



shared data

valuePresent (boolean)

value  $\rightarrow$  initialized to false

also shared

mu - a lock (mutex)

cv - a condition variable

producer

lock(mu)

while (if) (valuePresent) {

wait(cv, mu)

}

value = newValue;

valuePresent = ~~is~~ true;

signal(cv)

unlock(mu)

consumer

lock(mu)

while (if) (!valuePresent) {

wait(cv, mu)

}

newValue = value;

valuePresent = false;

signal(cv)

unlock(mu)

note: when wait is called, ~~is~~ lock is released (caller must own  
a single lock)  
when signal is called, ~~first~~ waiter is released

but newly released waiter must wait to re-acquire lock

bad things can happen:

race condition

deadlock - all threads are blocked

livelock - threads spend so much time responding to each other's actions that they cannot make progress on their work

starvation - some threads dominate access to shared resource so that other threads cannot access it